

Government College Kheri Chopta, Hisar

Lesson Plan 2023-24 (Even Semester)

Name of Teacher: **Sh. Dinesh**

Class: **BA-IIInd (IV Sem.)**

Subject: Computer Networks

Paper: **BACS-205 (Theory)**

Unit	Description of Chapter / Topics	Duration	Assignment / Test
Unit-1	Introduction to Computer Communications and Networking Technologies, Uses of Computer Networks, Network Devices, Nodes, and Hosts, Types of Computer Networks and their Topologies, OSI Reference Model, TCP/IP Reference Model.	2 nd January to 20 th January	Assignment-1
Unit-2	Analog and Digital Communications Concepts: Representing Data as Analog Signals, Representing Data as Digital Signals, Data Rate and Bandwidth, Capacity, Baud Rate; Digital Carrier Systems; Guided and Wireless Transmission Media; Communication Satellites; Switching and Multiplexing.	21 st January to 20 th February	Test-1
Unit-3	Data Link Layer: Framing, Flow Control, Error Control, Error Detection and Correction, Sliding Window Protocols, Media Access Control, Random Access Protocols, Token Passing Protocols, Token Ring, Ethernet, gigabit Ethernet, token ring, FDDI, Bluetooth and Wi-Fi.	22 nd February to 22 nd March	Assignment-2
Unit-4	Network Layer and Routing Concepts: Virtual Circuits and Datagrams, Routing Algorithms, Flooding, Shortest Path Routing, Distance Vector Routing, Link State Routing, Hierarchical Routing, Congestion Control Algorithms, Internetworking, IPV4 and IPV6.	1 st April to 20 th April	Test-2
Revision		21 st April to Exam Date	

Government College Kheri Chopta, Hisar

Lesson Plan 2023-24 (Even Semester)

Name of Teacher: **Sh. Dinesh**

Class: **BA-IIInd (IV Sem.)**

Subject: Software Engineering

Paper: **BACS-204 (Theory)**

Unit	Description of Chapter / Topics	Duration	Assignment / Test
Unit-1	<p>Introduction: Program vs. Software, Software Engineering paradigms, Software Crisis – problem and causes.</p> <p>Phases in Software development: Requirement, Analysis, Software Design, Coding, Testing, Maintenance.</p> <p>Software Development Process Models: Waterfall, Prototype, Evolutionary and Spiral models.</p>	2 nd January to 20 th January	Assignment-1
Unit-2	<p>Software Requirement Analysis and Specifications: Feasibility Study Software Requirements, Need for SRS, Characteristics of an SRS, Components of an SRS, Structure of a requirements document, validation and metrics. Problem Analysis, Data Flow Diagram, Data Dictionary, Decision table, Decision trees</p>	21 st January to 20 th February	Test-1
Unit-3	<p>Software Project Planning: Process Planning, Effort estimation, COCOMO model, Project scheduling and Staffing, team structure, Software configuration management, Quality assurance plans, Risk Management, Project monitoring plans.</p> <p>Software Implementation and Maintenance: Type of maintenance, Management of Maintenance, Maintenance Process, maintenance characteristics.</p>	22 nd February to 22 nd March	Assignment-2
Unit-4	<p>Testing : Testing fundamentals, Error, Fault, and Failure, Test Oracle, Test Case and Test Criteria, Psychology of testing, Black Box Testing, Equivalence Class Partitioning, Boundary value analysis, Cause effect graphing, White box testing , Control flow based criteria, level of testing, Unit testing, Integration testing, System testing, Validation testing, alpha, beta, and Acceptance testing.</p>	Ist April to 20 th April	Test-2
Revision		21 st April to Exam Date	

Government College Kheri Chopta, Hisar

Lesson Plan 2023-24 (Even Semester)

Name of Teacher: Dinesh

Class: BACS- IInd Sem

Subject Data Structure

Unit	Description of Chapter / Topics	Duration	Assignment / Test
Unit-1	Introduction: Elementary data organization, Data Structure definition, Data type vs. Data structure, Categories of data structure, Data structure operations, Applications of data structure, Algorithms complexity and time-space trade-off, Big-O notation..	1 st week of January to last week of January	
Unit-2	Strings: Introduction, Storing strings, String operations. Array: Introduction, Linear Arrays, Representation of linear array in memory, Traversal, Insertion, Deletion in an array, Multi-dimensional arrays.	1 st week of February to 4 th week of February	Ist Assignment in 4th week of February
Unit-3	Linked List: Introduction, Array vs. Linked List, Representation of Linked lists in memory, Traversal, Insertion, Deletion and Searching in a Linked List, Header Linked List, Circular Linked List, TwoWay Linked List, and Applications of Linked Lists. Stack: Introduction, Array and Linked representation of stack, operations on stack, Applications of stack: Polish Notation, Recursion	1 st week of march to third week of march	Minor test in the last week of march
Unit-4	Stack: Introduction, Array and linked representation of stacks, Operations on stacks, Applications of Stacks: Polish Notation, Recursion. Queue: Introduction, Array and linked representation of Queue, Operations on Queues, Dequeues, Priority Queues, Applications of Queues.	4 th week of march to second week of April	2 nd Assignment in the 2 nd week of April
Revision		3 rd week of April	

Government College Kheri Chopta, Hisar

Lesson Plan 2023-24 (Even Semester)

Name of Teacher: Dinesh

Class: B.A CS IInd Sem

Subject: (BACS – 122) Computer Organization

Unit	Description of Chapter / Topics	Duration	Assignment / Test
Unit-1	Data Representation: Number Systems: Decimal, Binary, Octal, Hexadecimal, Conversion from one number system to other; Binary arithmetic operations, Representation of Negative Numbers: 1's complement and 2's complement; fixed and floating point representation, character representation (BCD, EBCDIC and ASCII Code), BCD number system; Weighted Codes, Self Complementing Code, Excess-3 code, Gray and Cyclic code.	1 st week of January to last week of January	
Unit-2	Boolean Algebra: Introduction, Definition, Postulates of Boolean Algebra, Fundamental Theorems of Boolean Algebra; Duality Principle, Demorgan's Theorems, Boolean Expressions and Truth Tables, Standard SOP and POS forms, Canonical representation of Boolean expressions, Simplification of Boolean Expressions using theorems of Boolean algebra, Minimization Techniques for Boolean Expressions using Karnaugh Map. Logic Gates: AND, OR, NOT, NOR, NAND & XOR Gates and their Truth tables.	1 st week of February to 4 th week of February	Ist Assignment in 4th week of February
Unit-3	Combinational Circuits: Half Adder & Full Adder, Half Subtractor & Full Subtractor, Adder & Subtractor, decoders, multiplexors. Realization of Boolean expressions using decoders and multiplexor. Sequential Circuits: Flip-Flops, Types- RS, T, D, JK and Master-Slave JK flip flop, Triggering of Flip Flops; Flip Flop conversions, Shift Registers, Synchronous and Asynchronous Counters.	1 st week of march to third week of march	Minor test in the last week of march
Unit-4	Basic Computer Organization and Design: Register Organization, Bus system, instruction set, timing and control, instruction cycle, memory reference, input-output and interrupt. Programming the Basic Computer: Instruction formats, addressing modes, instruction codes. Input-output Organization: Peripheral devices, I/O interface, Modes of data transfer, Direct Memory Access.	4 th week of march to second week of April	2 nd Assignment in the 2 nd week of April
Revision		3 rd week of April	